

IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

<u>First Named Inventor: Thomas J Heller JR :</u>	<u>GROUP ART UNIT 2187</u>
<u>Pat Appln: Serial No. 10/603252 :</u>	<u>Examiner: Jesse David Diller</u>
<u>Filed: June 25, 2003 :</u>	<u>Date: October 23, 2007</u>
<u>Title: Multiprocessor System with Dynamic Cache Coherency Regions :</u>	
<u>Attorney Docket POU920030046US1 :</u>	<u>Confirmation No. 3685</u>
<u>Deposit Account: 09-0463 :</u>	

Mailing Address: Lynn L. Augspurger, Attorney, Reg No. 24,227 :
IBM Intellectual Property Law
2455 South Road, P386
Poughkeepsie, NY 12601

SUPPLEMENT REFILING AMENDMENT (Responsive to OA Dated 4/20/2007
and the communication of 9/28/2007)

EXPEDITED PROCESSING REQUESTED

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

A. Introductory Comments:

In reply to the Official action mailed 4/20/2007 and the communication of the examiner mailed 9/28/2007 Applicants respectfully request entry of this Supplement Refiling Amendment to place the application in condition for allowance in light of the remarks and these amendments. (The Commissioner is also hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 09-0463. Those expected are already part of the separate fee authorization.)